# JOSHUA VISZLAI

Email: viszlai@uchicago.edu  $\circ$  Phone: 678-662-1158 Website: jviszlai.github.io  $\circ$  GitHub: jviszlai

#### **EDUCATION**

Ph.D., University of Chicago (Expected Spring 2026) Chicago, IL Department of Computer Science 2020 - Present Thesis: Software/Hardware Co-Design of Fault-Tolerant Quantum Systems Advisor: Frederic T. Chong GPA: 4.0 B.Sc., Georgia Institute of Technology Atlanta, GA Computer Science (Systems & Architecture, Intelligence) w/ Physics Minor 2016 - 2020 Thesis: An Analysis of Register Allocation Techniques in the Context of a RISC-V Processor Advisor: Vivek Sarkar GPA: 4.0 PROFESSIONAL EXPERIENCE PhD Candidate, University of Chicago, Computer Science Dept., 2020-Present Chicago, IL Quantum Error Correction Consultant, Inflequion, 2025-Present Chicago, IL Undergraduate Researcher Georgia Tech, Computer Science Dept., 2019-2020 Atlanta, GA Software Engineering Intern, Microsoft, Visual Studio, Summer 2019, 2020 Redmond, WA Undergraduate Researcher Georgia Tech, Computer Science Dept., Fall 2018 Atlanta, GA AWARDS AND HONORS Best Poster Honorable Mention, International Conference on Quantum Error Correction 2025 for Erasure Minesweeper with Chadwick, Teo, Yang, and Chong Best Paper (Systems 1<sup>st</sup> Place), Intl. Conference on Quantum Computing and Engineering 2024for Averting multi-qubit burst errors in surface code magic state factories with Chadwick et al. IEEE Micro Top Picks in Computer Architecture of 2022, Honorable Mention 2023 for SupermarQ: A Scalable Quantum Benchmark Suite with Tomesh et al. Best Paper, International Symposium on High-Performance Computer Architecture 2022 for SupermarQ: A Scalable Quantum Benchmark Suite with Tomesh et al. Crerar Fellowship 2020-2021 University of Chicago Graduated with Highest Honors 2020 Georgia Institute of Technology Zell Miller Scholarship (Full Tuition) 2016-2020 Georgia Institute of Technology

#### REFEREED PUBLICATIONS

decoder-bench: Benchmarking Decoders for Quantum Error Correction October 2025
 Maurya, J. Viszlai, N. Raveendran, P. Das, S. Tannu
 in IEEE International Symposium on Workload Characterization (IISWC)

2. Matching Generalized-Bicycle Codes to Neutral Atoms for Low-Overhead Fault-Tolerance September 2025

<u>J. Viszlai</u>, W. Yang, S. F. Lin, J. Liu, N. Nottingham, J. M. Baker, F. T. Chong in *IEEE International Conference on Quantum Computing and Engineering* (**QCE**)

- 3. Erasure Minesweeper: exploring hybrid-erasure surface code architectures for efficient quantum error correction September 2025
  - J. D. Chadwick, M. H. Teo, <u>J. Viszlai</u>, W. Yang, F. T. Chong in *IEEE International Conference on Quantum Computing and Engineering* (**QCE**)
- 4. SWIPER: Minimizing Fault-Tolerant Quantum Program Latency via June 2025 Speculative Window Decoding

<u>J. Viszlai,</u> J. D. Chadwick, S. Joshi, G. S. Ravi, Y. Li, F. T. Chong in *International Symposium on Computer Architecture* (**ISCA**)

- 5. Interleaved Logical Qubits in Atom Arrays March 2025 <u>J. Viszlai, S. F. Lin, S. Dangwal, C. Bradley, V. Ramesh, J. M. Baker, H. Bernien, F. T. Chong in IEEE International Symposium on High-Performance Computer Architecture (HPCA)</u>
- 6. Averting multi-qubit burst errors in surface code magic
  state factories
  J. D. Chadwick, C. Kang, J. Viszlai, S. F. Lin, F. T. Chong

in *IEEE International Conference on Quantum Computing and Engineering* (QCE)

Best Paper Award: 1<sup>st</sup> Place Quantum Systems Software

7. Codesign of quantum error-correcting codes and modular chiplets
in the presence of defects

April 2024

S. F. Lin, <u>J. Viszlai</u>, K. N. Smith, G. S. Ravi, C. Yuan, F. T. Chong, B. J. Brown in *ACM International Conference on Architectural Support for Programming Languages and Operating Systems* (**ASPLOS**)

- 8. Fast Fingerprinting of Cloud-based NISQ Quantum Computers May 2023 K. N. Smith, <u>J. Viszlai</u>, L. M. Seifert, J. M. Baker, J. Szefer, F. T. Chong in *IEEE International Symposium on Hardware Oriented Security and Trust* (HOST)
- 9. Training Quantum Boltzmann Machines with Coresets

  J. Viszlai, T. Tomesh, P. Gokhale, E. Anschuetz, F. T. Chong
  in IEEE International Conference on Quantum Computing and Engineering (QCE)
- 10. SupermarQ: A Scalable Quantum Benchmark Suite

  T. Tomesh, P. Gokhale, V. Omole, G. S. Ravi, K. Smith, <u>J. Viszlai</u>, X.-C. Wu, N. Hardavellas, M. R. Martonosi, F. T. Chong in *IEEE International Symposium on High-Performance Computer Architecture* (HPCA)

  Best Paper Award, IEEE Micro Top Picks Honorable Mention
- Adapting Quantum Approximation Optimization Algorithm October 2021 (QAOA) for Unit Commitment
   S. Koretsky, P. Gokhale, J. M. Baker, <u>J. Viszlai</u>, H. Zheng, N. Gurung, R. Burg, E. A. Paaso, A. Khodaei, R. Eskandarpour, F. T. Chong in *IEEE International Conference on Quantum Computing and Engineering* (QCE)

### UNDER REVIEW

- 1. qSIEVE: Efficient qLDPC Memory via Systolic Movement in Atom Arrays J. Viszlai, W. Yang, S. F. Lin, J. Liu, N. Nottingham, J. M. Baker, F. T. Chong
- 2. Propagation-Aware Compilation of Syndrome Measurement Circuits <u>J. Viszlai</u>, S. Maurya, S. Tannu, M. Martonosi, F. T. Chong

1. Racing Camels in a Quantum Game of Camel Up June 2025 ACM SIGARCH Blog with F. T. Chong 2. 5 Year Update to the Next Steps in Quantum Computing January 2024 K. Brown, F. T. Chong, K. N. Smith, T. Conte, A. Adams, A. Dalvi, C. Kang, J. Viszlai **TEACHING** Co-Instructor: Quantum Computer Systems (CMSC 229/329) Spring '23, Spring '25 Computer Science Department, University of Chicago, (with F. T. Chong) Teaching Assistant and Course Manager: Quantum Computer Systems Design 2021-Present MOOC on edX, University of Chicago Teaching Assistant: Computer Architecture (CMSC 222) Fall '22 Computer Science Department, University of Chicago Teaching Assistant: Quantum Computer Systems (CMSC 229/329) Spring '22 Computer Science Department, University of Chicago **Instructor**: Software Engineering Major, Video Game Development Minor Summer '21 Georgia Governor's Honors Program Head Teaching Assistant: Computer Organization and Programming (CS 2110) 2019-2020 Computer Science Department, Georgia Institute of Technology **Teaching Assistant**: Computer Organization and Programming (CS 2110) 2018-2019 Computer Science Department, Georgia Institute of Technology INVITED TALKS 1. Enabling Programming Abstractions for More Effective September 2025 Quantum Development Invited panelist at QCE '25 workshop Alburquerque, NM 2. Device-aware Quantum Software September 2025 Invited speaker and panelist at QCE '25 workshop Alburquerque, NM 3. Decoding the Performance Gap: Propagation-Aware June 2025 Compilation of Stabilizer Circuits Workshop talk at Workshop on Fault-Tolerant Quantum Computer Tokyo, Japan System Architecture (FTQCSA) 4. Speculative Window Decoding for Fault-Tolerant June, April, February 2025 **Quantum Programs** • Conference talk at ISCA '25 Tokyo, Japan • Invited talk at Northwestern University Chicago, IL • Seminar talk at HQAN Research Coordination Talks Chicago, IL

Invited keynote speaker and panelist for Workshop on Architecting Las Vegas, NV Error Corrected Quantum Computers (ARQTEC)

7. Simulating Generalized-Bicycle Codes for Atom Arrays September 2025, January 2024

• Conference talk at QCE '25

Fault-Tolerance via Co-Design

Conference talk at HPCA '25

5. Interleaved Logical Qubits in Atom Arrays

6. Entangling Codes with Hardware: Reducing the Cost of

March 2025

March 2025

Las Vegas, NV

	• Invited talk at Infleqtion	Chicago, IL
8.	Training Quantum Boltzmann Machines with Coresets Conference talk at QCE '22	September 2022 Broomfield, CC
OTHER	R TALKS AND PRESENTATIONS	
1.	Speculative Window Decoding for Fault-Tolerant Quantum Programs	April, March 2025
	• Talk at UChicago's Quantum Information Science and Engineering Ser	minar Chicago, II
	• Talk at APS Global Physics Summit	Anaheim, CA
2.	Introduction to Quantum Error Correction Tutorial talk at I too can Quantum! (I2Q)  But	June 2024 nenos Aires, Argentina
3.	Simulating Generalized Bicycle Codes for Atom Arrays	March, February 2024
	• Talk at APS March Meeting	Minneapolis, MN
	• Talk at UChicago's Quantum Information Science and Engineering Ser	ninar Chicago, II
4.	Quantum Error Correction and Graph Processing for Decoding Tutorial talk at I too can Quantum! (I2Q)	June 2023 Orlando, FI
5.	Evaluating Surface Code Schemes for Neutral Atom Devices Talk at APS March Meeting	March 2023 Las Vegas, NV
PROFE	SSIONAL SERVICE	
Org	anizer:	
•	I too can Quantum! at ISCA 2023, 2024	
Con	aference Committees:	
•	IEEE International Symposium on High-Performance Computer Architectu	are (HPCA) 2026
•	IEEE International Conference on Quantum Computing and Engineering (	QCE) 2024, 2025
Jou	rnal Refereeing:	
•	Physical Review X Quantum (PRX Quantum)	
•	Nature Partner Journals (npj Quantum Information)	
OPEN-	SOURCE SOFTWARE	
•	qLDPCOrg/qLDPC Broad circuit-level simulation support for quantum error correcting codes	2025-Presen
•	jviszlai/swiper Round-level windowed decoding simulator for lattice surgery programs	2025-Presen

## COMMUNITY OUTREACH

• Science Works at Griffin Museum of Science and Industry Engaged with community members at UChicago CANON Lab station	October 2024 Chicago, IL
• Peter & Judy Kovler Career Conference for High School Students Panelist and networking lunch participant	June 2023 Chicago, IL
South Side Science Festival	September 2022

• South Side Science Festival September 2022 Engaged with community members at UChicago CANON Lab station Chicago, IL

• RoboJackets Outreach Organized community volunteering and mentorship for K-12 students	2016-2020 Atlanta, GA
• Georgia FIRST Robotics FRC event volunteer	2016-Present Georgia
• Georgia Governor's Honors Program Computer Science application reviewer and interviewer	2018-Present Georgia